

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A circuit substrate, comprising:
a substrate;
a plurality of terminals formed ~~on the~~ over the substrate; and
at least one ~~resistance formed between the terminals adjacent one~~
~~another; resistance;~~
the plurality of terminals including an analog terminal connected to an analog signal lines to supply analog signals, and digital terminals connected to digital signal lines to supply digital signals; and
the one resistance having at least one end connected to the analog terminal, and having a resistance value greater than another resistance connected between the digital ~~terminals; terminals;~~
the resistance and the another resistance being formed of a semiconductor film.
2. (Currently Amended) A circuit substrate, comprising:
a substrate;
a plurality of terminals formed ~~on the~~ over the substrate; and
at least one ~~resistance formed between the terminals adjacent one~~
~~another; resistance;~~
the plurality of terminals including a first terminal connected to a data lines to supply data signals, and second terminals connected to control lines to supply control signals; and

the one resistance having at least one end connected to the first terminal, and having a resistance value greater than another resistance connected between the second terminals adjacent one ~~another~~ another, the resistance and the another resistance being formed of a semiconductor film.

3. (Currently Amended) A circuit substrate, comprising:

a substrate;

a plurality of emitting elements formed in a display area, each of the plurality of emitting elements having a first electrode, a second electrode, and an emitting layer between the first electrode and the second electrode, and the first electrode being a common electrode of the plurality of emitting elements;

a common electrode line formed on the perimeter of the display area, the common electrode line connected to the common electrode;

analog signal lines to supply analog signals;

digital signal lines to supply digital signals;

an analog terminal formed ~~on the~~ over the substrate, the analog terminal being connected to one of the analog signal lines;

a digital terminal formed ~~on the~~ over the substrate, the digital terminal being connected to one of the digital signal lines;

a first resistor connected between the analog terminal and the common electrode line; and

a second resistor connected between the digital terminal and the common electrode line,

the first resistor having a resistance value greater than the second resistor.

4. (Currently Amended) A circuit substrate, comprising:

a substrate;

a plurality of emitting elements formed in a display area, each of the plurality of emitting elements having a first electrode, a second electrode, and an emitting layer between the first electrode and the second electrode, and the first electrode being a common electrode of the plurality of emitting elements;

a common electrode line formed on the perimeter of the display area, the common electrode line connected to the common electrode;

a plurality of terminals formed ~~on the~~ over the substrate, the plurality of terminals including a first terminal and a second terminal;

a first resistor connected between the first terminal and the second terminal;
and

a second resistor connected between the common electrode line and the first terminal.

5. (Previously Presented) The circuit substrate according to Claim 4,
the first resistor having a resistance value greater than the second resistor.

6. (Currently Amended) The circuit substrate according to Claim 5, the plurality of terminals including ~~analog terminals~~ an analog terminal connected to ~~analog signal lines~~ an analog signal line to supply analog signals, and digital terminals connected to digital signal lines to supply digital signals; and

both the first ~~resistance~~ resistor and the second ~~resistance~~ resistor which have at least one end connected to the analog terminal, having resistance values greater than both the first ~~resistance~~ resistor which is connected between the digital terminals, and the second ~~resistance~~ resistor which is connected between the digital terminal and the common electrode line.

7. (Previously Presented) The circuit substrate according to Claim 1, further comprising:

electric power terminals connected to a power source; and
resistances formed between the electric power terminals and adjacent non-electric power terminals formed for purposes other than supplying power.

8. (Previously Presented) The circuit substrate according to Claim 7, the resistance having a resistance value equal to or less than the resistance connected to other non-electric power terminals.

9-12. (Canceled)

13. (Previously Presented) The circuit substrate according to Claim 1, the resistance including a protection circuit configuration employing PN junction configurations with reverse polarity.

14. (Previously Presented) An electro-optical device, comprising:
the circuit substrate according to Claim 1.

15. (Previously Presented) An electronic apparatus, comprising:
the electro-optical device according to Claim 14.

16-17. (Canceled)

18. (Currently Amended) A circuit substrate, comprising:
a substrate;
analog signal lines to supply analog signals;
digital signal lines to supply digital signals;
an analog terminal formed ~~on the~~over the substrate, the analog terminal being
connected to one of the analog signal lines;
digital terminals formed ~~on the~~over the substrate, each of the digital terminals
being connected to one of the digital signal lines respectively;
a first resistor having at least one end connected to the analog terminal; and

a second resistor connected between the digital terminals, the first resistor and the second resistor being formed of a semiconductor film,

the first resistor having a resistance value greater than the second resistor.

19. (Canceled)

20. (Previously Presented) A circuit substrate comprising:

a substrate;

a plurality of emitting elements formed in a display area, each of the plurality of emitting elements having a first electrode, a second electrode, and an emitting layer between the first electrode and the second electrode, and the first electrode being a common electrode of the plurality of emitting elements;

a common electrode line formed on the perimeter of the display area, the common electrode line connected to the common electrode;

analog signal lines to supply analog signals;

digital signal lines to supply digital signals;

analog terminals formed on the substrate, each of the analog terminals being connected to one of the analog signal lines respectively;

digital terminals formed on the substrate, each of the digital terminals being connected to one of the digital signal lines respectively;

a first resistor connected between the common electrode line and one of the analog terminals;

a second resistor connected between the analog terminals;

a third resistor connected between the digital terminals; and

a fourth resistor connected between the common electrode line and one of the digital terminals,

the first resistor having a resistance value greater than both of the third resistor and the fourth resistor, and the second resistor having a resistance value greater than both of the third resistor and the fourth resistor.

21-23. (Canceled)

24. (Previously Presented) The circuit substrate according to Claim 18, the first resistor and the second resistor including a protection circuit configuration employing PN junction configurations with reverse polarity.

25. (Previously Presented) An electro-optical device, comprising:
the circuit substrate according to Claim 18.

26. (Previously Presented) An electronic apparatus, comprising:
the electro-optical device according to Claim 25.